Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK3067

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• Low drain–source ON resistance : R_{DS} (ON) = 4.2 Ω (typ.)

• High forward transfer admittance $: |Y_{fs}| = 1.7 \text{ S (typ.)}$

• Low leakage current : $IDSS = 100 \mu A (max) (VDS = 600 V)$

• Enhancement-mode : $V_{th} = 2.0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Maximum Ratings (Tc = 25°C)

Charac	teristics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	600	V
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	600	V
Gate-source voltage	ge	V _{GSS}	±30	V
Drain current	DC (Note 1)	I _D	2	Α
	Pulse (t = 1 ms) (Note 1)	I _{DP}	5	А
	Pulse (t = 100 µs) (Note 1)	I _{DP}	8	А
Drain power dissipa	ation	P _D	25	W
Single pulse avalanche energy (Note 2)		E _{AS}	93	mJ
Avalanche current		I _{AR}	2	Α
Repetitive avalanche energy (Note 3)		E _{AR}	2.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55~150	°C

Weight: 1.9 g (typ.)

Electrical Characteristics (Tc = 25°C)

Characteristics	Symbol	Max	Unit
Thermal reverse, channel to case	R _{th (ch-c)}	5.0	°C/W
Thermal reverse, channel to ambient	R _{th (ch-a)}	62.5	°C / W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 41 mH, R_G = 25 Ω , I_{AR} = 2 A

Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device. Please handle with caution.

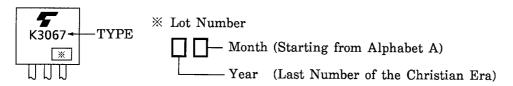
Electrical Characteristics (Tc = 25°C)

Charac	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	rrent	I_{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source bre	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold v	roltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 1 A	_	4.2	5.0	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1 A	0.8	1.7	_	S
Input capacitano	е	C _{iss}		_	380	_	
Reverse transfer capacitance C_{rss} $V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	40	_	pF	
Output capacitance		C _{oss}		_	120	_	
Switching time	Rise time	t _r	V_{GS} V_{OV} V_{OUT} V_{OUT} V_{OUT} V_{OUT} V_{DD} V_{OUT}	_	15	_	
	Turn-on time	t _{on}		_	25	-	ns
	Fall time	t _f		_	20	ı	115
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{W}} = 10 \mu s$	_	80		
Total gate charge (Gate-source plus gate-drain)		Qg			9		
Gate-source charge		Q _{gs}	$V_{DD} \approx 480 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}$		5	_	nC
Gate-drain ("miller") charge		Q_{gd}			4	_	

Source-Drain Ratings and Characteristics (Tc = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	2	Α
Pulse drain reverse current (Note 1)	I _{DRP}	t = 1 ms	_	_	5	Α
	I _{DRP}	t = 100 μs	_	_	8	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 2 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 2 A, V _{GS} = 0 V dI _{DR} / dt = 100 A / μs	_	1000	_	ns
Reverse recovery charge	Q _{rr}	dl _{DR} / dt = 100 A / μs	_	5.0		μC

Marking



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