# **High Voltage Transistor**

# **PNP Silicon**

### Features

• This is a Pb–Free Device\*

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CEO</sub>	-350	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	-350	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	-6.0	Vdc
Collector Current – Continuous	۱ <sub>C</sub>	-500	mAdc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above = $25^{\circ}C$	P <sub>D</sub>	625 5.0	mW mW/°C
Total Device Dissipation @ $T_C = 25^{\circ}C$ Derate above 25°C	PD	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

### THERMAL CHARACTERISTICS

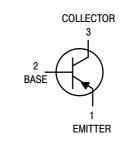
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



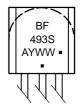
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### MARKING DIAGRAM



A = Assembly Location Y = Year WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

### **ORDERING INFORMATION**

Device	Package	Shipping
BF493SG	TO-92 (Pb-Free)	5000 Units / Bulk

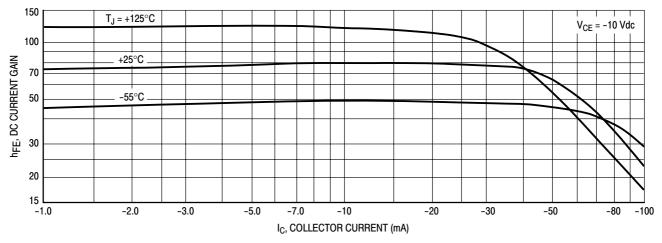
\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## **BF493S**

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = $25^{\circ}$ C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS		•	•	
Collector – Emitter Breakdown Voltage (Note 1) $(I_{C} = -1.0 \text{ mAdc}, I_{B} = 0)$	V <sub>(BR)CEO</sub>	-350	_	Vdc
Collector – Base Breakdown Voltage $(I_C = -100 \ \mu Adc, I_E = 0)$	V <sub>(BR)CBO</sub>	-350	-	Vdc
Emitter – Base Breakdown Voltage ( $I_E = -100 \ \mu Adc, I_C = 0$ )	V <sub>(BR)EBO</sub>	-6.0	-	Vdc
Collector Cutoff Current (V <sub>CE</sub> = -250 Vdc)	I <sub>CES</sub>	-	-10	nAdc
Emitter Cutoff Current ( $V_{EB} = -6.0 \text{ Vdc}, I_C = 0$ )	I <sub>EBO</sub>	-	0.1	μAdc
Collector Cutoff Current $(V_{CB} = -250 \text{ Vdc}, I_E = 0, T_A = 25^{\circ}\text{C})$ $(V_{CB} = -250 \text{ Vdc}, I_E = 0, T_A = 100^{\circ}\text{C})$	Ісво		-0.005 -1.0	μAdc
ON CHARACTERISTICS	·			
DC Current Gain (I <sub>C</sub> = -1.0 mAdc, V <sub>CE</sub> = -10 Vdc) (I <sub>C</sub> = -10 mAdc, V <sub>CE</sub> = -10 Vdc)	h <sub>FE</sub>	25 40		-
Collector – Emitter Saturation Voltage ( $I_C = -20 \text{ mAdc}, I_B = -2.0 \text{ mAdc}$ )	V <sub>CE(sat)</sub>	-	-2.0	Vdc
Base – Emitter On Voltage ( $I_C = -20 \text{ mA}, I_B = -2.0 \text{ mA}$ )	V <sub>BE(sat)</sub>	_	-2.0	Vdc
DYNAMIC CHARACTERISTICS		1		
Current-Gain – Bandwidth Product ( $I_C = -10 \text{ mAdc}, V_{CE} = -20 \text{ Vdc}, f = 20 \text{ MHz}$ )	fT	50	-	MHz
Common–Emitter Feedback Capacitance $(V_{CB} = -100 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C <sub>re</sub>	_	1.6	pF

1. Pulse Test: Pulse Width  $\leq$  300 µs; Duty Cycle  $\leq$  2.0%.





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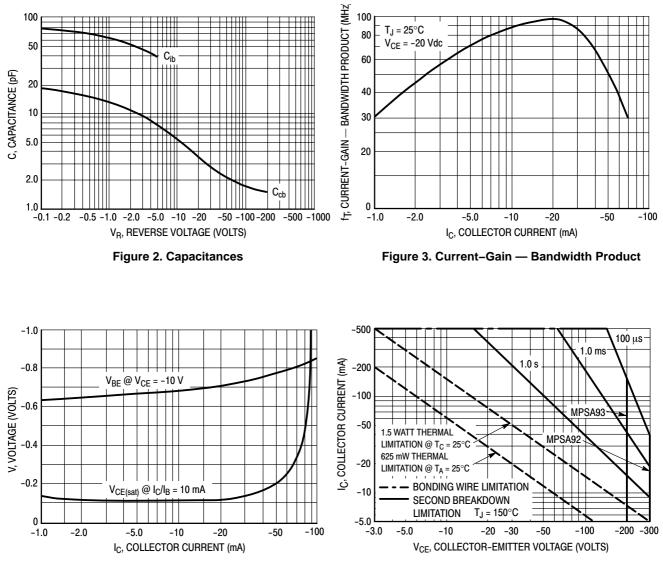


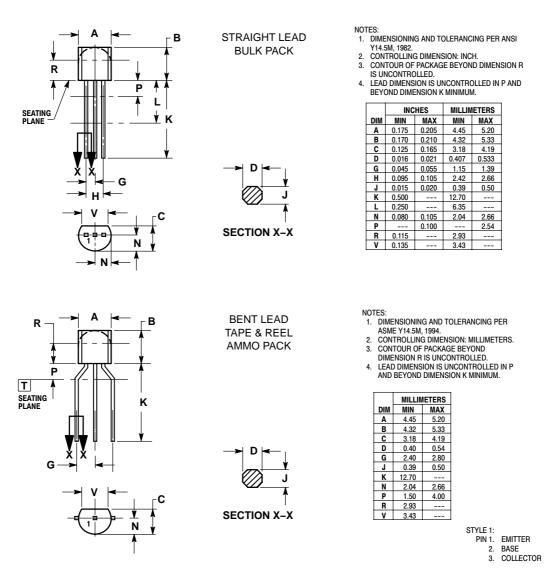
Figure 4. "On" Voltages

Figure 5. Active Region — Safe Operating Area

### **BF493S**

#### PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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