Silicon P-Channel MOS FET

# HITACHI

November 1996

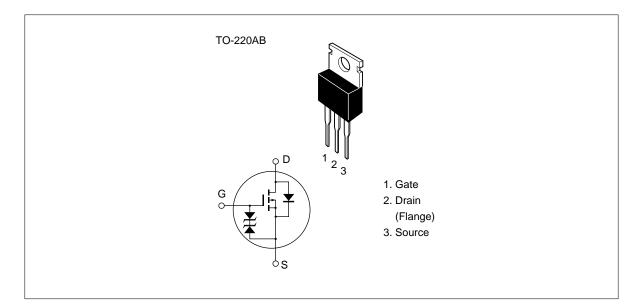
#### Application

High speed power switching

#### Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device
  - Can be driven from 5 V source
- Suitable for motor drive, DC-DC converter, power switch and solenoid drive

#### Outline



#### **Absolute Maximum Ratings** (Ta = 25°C)

Symbol	Ratings	Unit	
V <sub>DSS</sub>	-60	V	
V <sub>GSS</sub>	±20	V	
I <sub>D</sub>	-10	A	
I *1 D(pulse)	-40	A	
I <sub>DR</sub>	-10	A	
Pch*2	40	W	
Tch	150	°C	
Tstg	-55 to +150	°C	
	$V_{DSS}$ $V_{GSS}$ $I_D$ $I_{D(pulse)}^{*1}$ $I_{DR}$ $Pch^{*2}$ $Tch$	V         -60           V $\pm 20$ I $-10$ I $-40$ I $-10$ Pch*2         40           Tch         150	

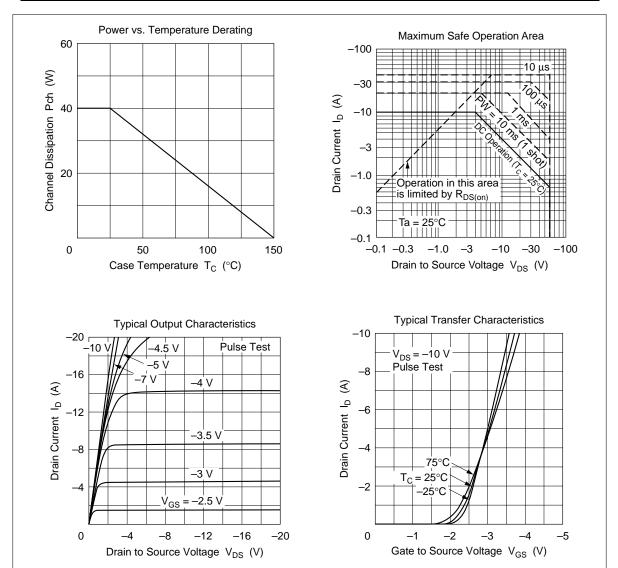
Notes 1.  $PW \le 10 \ \mu s$ , duty cycle  $\le 1\%$ 

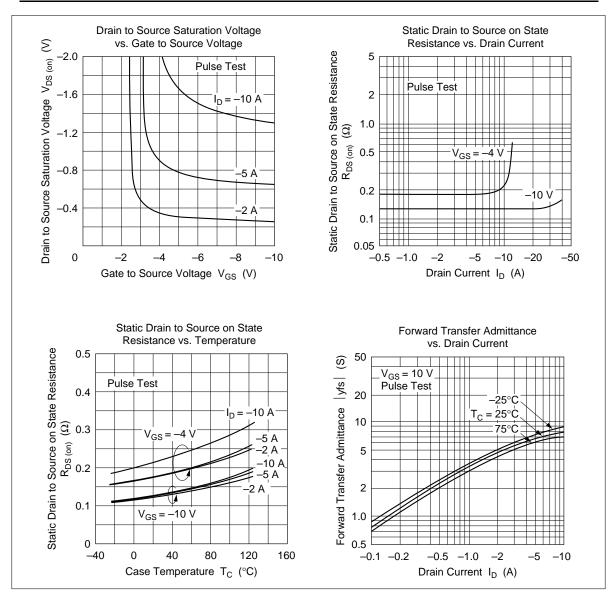
2. Value at T<sub>c</sub> =  $25^{\circ}$ C

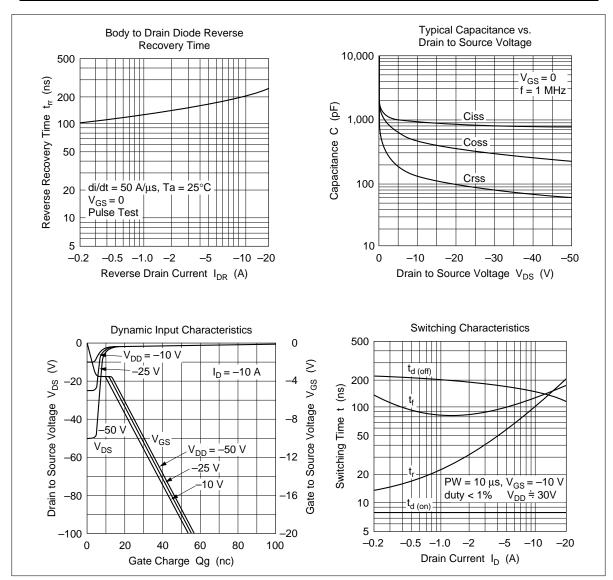
#### **Electrical Characteristics** (Ta = 25°C)

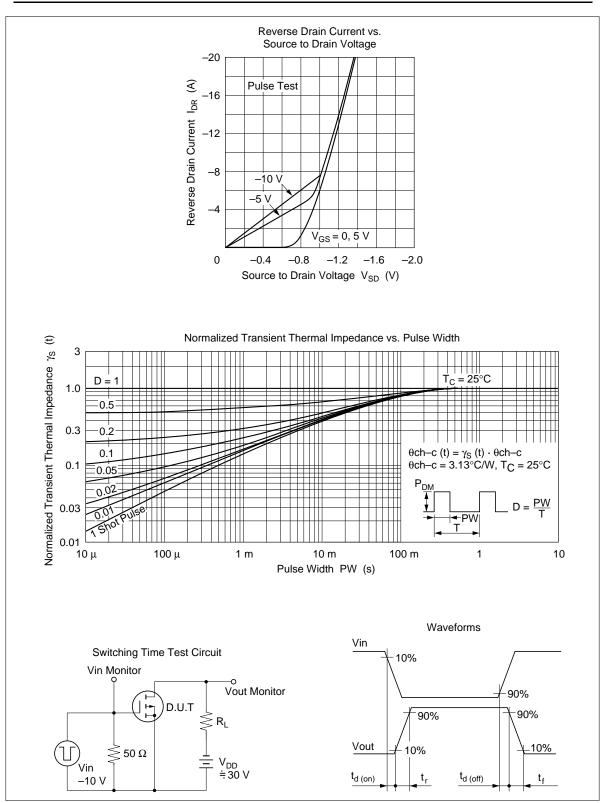
Symbol	Min	Тур	Max	Unit	Test conditions
$V_{(\text{BR})\text{DSS}}$	-60	_	_	V	$I_{\rm D} = -10$ mA, $V_{\rm GS} = 0$
$V_{(BR)GSS}$	±20	_	_	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
I <sub>GSS</sub>			±10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
I <sub>DSS</sub>			250	μA	$V_{\rm DS} = -50$ V, $V_{\rm GS} = 0$
V <sub>GS(off)</sub>	-1.0		-2.0	V	$I_{\rm D} = -1 \text{ mA}, V_{\rm DS} = -10 \text{ V}$
R <sub>DS(on)</sub>		0.13	0.18	Ω	$I_{\rm D} = -5$ A, $V_{\rm GS} = -10$ V <sup>*1</sup>
	_	0.18	0.25		$I_{\rm D} = -5$ A, $V_{\rm GS} = -4$ V <sup>*1</sup>
y <sub>fs</sub>	4.0	6.5		S	$I_{\rm D} = -5$ A, $V_{\rm DS} = -10$ V <sup>*1</sup>
Ciss		900		pF	$V_{DS} = -10 V, V_{GS} = 0,$
Coss		460	_	pF	f = 1 MHz
Crss	_	130	—	pF	_
t <sub>d(on)</sub>		8		ns	$I_{\rm D} = -5$ A, $V_{\rm GS} = -10$ V,
t,		65		ns	$R_{L} = 6 \Omega$
t <sub>d(off)</sub>		170		ns	
t <sub>f</sub>		105	_	ns	
$V_{\text{DF}}$	_	-1.1		V	$I_{F} = -10 \text{ A}, V_{GS} = 0$
t <sub>rr</sub>	_	200	_	ns	$I_{F} = -10 \text{ A}, V_{GS} = 0,$ $di_{F}/dt = 50 \text{ A}/\mu\text{s}$
	$V_{(BR)DSS}$ $V_{(BR)GSS}$ $I_{GSS}$ $I_{DSS}$ $V_{GS(off)}$ $R_{DS(on)}$ $Iy_{fs} $ $Cisss$ $Crss$ $Crss$ $t_{d(on)}$ $t_{r}$ $t_{d(off)}$ $t_{f}$ $V_{DF}$	$\begin{array}{c} V_{(BR)DSS} & -60 \\ \\ V_{(BR)GSS} & \pm 20 \\ \\ I_{GSS} & \\ I_{DSS} & \\ \\ V_{GS(off)} & -1.0 \\ \\ R_{DS(on)} & \\ \\ \hline \\ V_{GS(off)} & -1.0 \\ \\ \hline \\ R_{DS(on)} & \\ \\ \hline \\ V_{I}f_{I} & 4.0 \\ \\ \hline \\ Ciss & \\ \hline \\ Ciss & \\ \hline \\ Coss & \\ \hline \\ Coss & \\ \hline \\ Crss & \\ \hline \\$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c } V_{(BR)DSS} & -60 & - & - \\ \hline V_{(BR)GSS} & \pm 20 & - & - \\ \hline I_{GSS} & - & - & \pm 10 \\ \hline I_{DSS} & - & - & 250 \\ \hline V_{GS(off)} & -1.0 & - & -2.0 \\ \hline R_{DS(on)} & - & 0.13 & 0.18 \\ \hline - & 0.13 & 0.18 \\ \hline - & 0.18 & 0.25 \\ \hline Iy_{fs} & 4.0 & 6.5 & - \\ \hline Ciss & - & 900 & - \\ \hline Coss & - & 460 & - \\ \hline Crss & - & 130 & - \\ \hline Crss & - & 130 & - \\ \hline t_{d(off)} & - & 8 & - \\ \hline t_{f} & - & 105 & - \\ \hline V_{DF} & - & -1.1 & - \\ \end{array}$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

Note 1. Pulse test









When using this document, keep the following in mind:

- 1. This document may, wholly or partially, be subject to change without notice.
- 2. All rights are reserved: No one is permitted to reproduce or duplicate, in any form, the whole or part of this document without Hitachi's permission.
- 3. Hitachi will not be held responsible for any damage to the user that may result from accidents or any other reasons during operation of the user's unit according to this document.
- 4. Circuitry and other examples described herein are meant merely to indicate the characteristics and performance of Hitachi's semiconductor products. Hitachi assumes no responsibility for any intellectual property claims or other problems that may result from applications based on the examples described herein.
- 5. No license is granted by implication or otherwise under any patents or other rights of any third party or Hitachi, Ltd.
- 6. MEDICAL APPLICATIONS: Hitachi's products are not authorized for use in MEDICAL APPLICATIONS without the written consent of the appropriate officer of Hitachi's sales company. Such use includes, but is not limited to, use in life support systems. Buyers of Hitachi's products are requested to notify the relevant Hitachi sales offices when planning to use the products in MEDICAL APPLICATIONS.

# HITACHI

#### Hitachi, Ltd.

Semiconductor & IC Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100, Japan Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

#### For further information write to:

Hitachi America, Ltd. Semiconductor & IC Div. 2000 Sierra Point Parkway Brisbane, CA. 94005-1835 U S A Tel: 415-589-8300 Fax: 415-583-4207 Hitachi Europe GmbH Electronic Components Group Continental Europe Dornacher Straße 3 D-85622 Feldkirchen München Tel: 089-9 91 80-0 Fax: 089-9 29 30 00 Hitachi Europe Ltd. Electronic Components Div. Northern Europe Headquarters Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA United Kingdom Tel: 0628-585000 Fax: 0628-778322 Hitachi Asia Pte. Ltd. 16 Collyer Quay #20-00 Hitachi Tower Singapore 0104 Tel: 535-2100 Fax: 535-1533

Hitachi Asia (Hong Kong) Ltd. Unit 706, North Tower, World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel: 27359218 Fax: 27306071

#### **HITACHI**