2SJ295

Silicon P-Channel MOS FET

HITACHI

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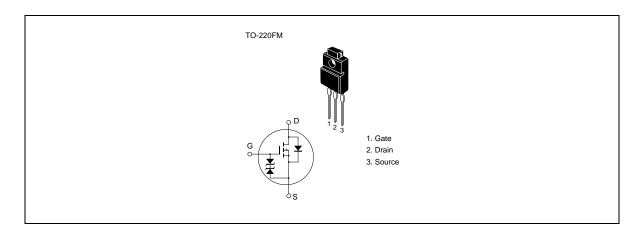
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for switching regulator, DC-DC converter
- Avalanche ratings

Outline



2SJ295

Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

| Item | Symbol | Ratings | Unit |
|---|--------------------|-------------|------|
| Drain to source voltage | V _{DSS} | -60 | V |
| Gate to source voltage | V _{GSS} | ±20 | V |
| Drain current | I _D | -30 | A |
| Drain peak current | I *1 D(pulse) | -120 | A |
| Body to drain diode reverse drain current | I _{DR} | -30 | A |
| Avalanche current | *3 | -30 | A |
| Avalanche energy | E _{AR} *3 | 77 | mJ |
| Channel dissipation | Pch*2 | 35 | W |
| Channel temperature | Tch | 150 | °C |
| Storage temperature | Tstg | -55 to +150 | °C |

Notes 1. PW \leq 10 µs, duty cycle \leq 1%

- 2. Value at $T_c = 25$ °C
- 3. Value at Tch = 25°C, Rg \geq 50 Ω

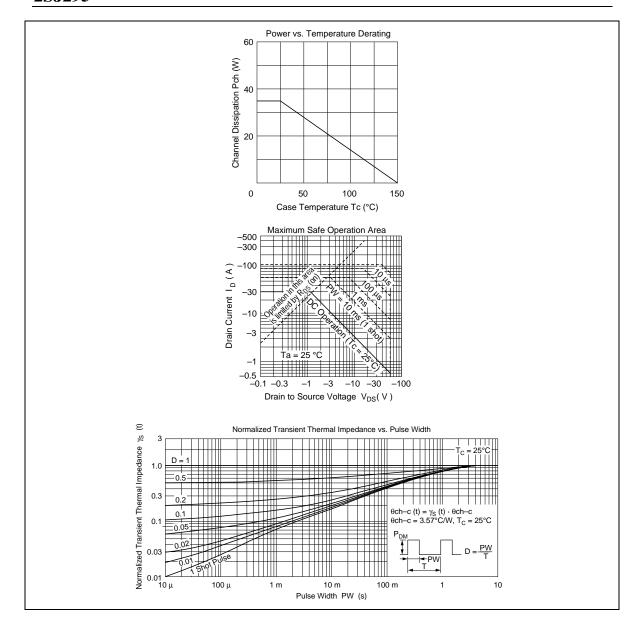
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Electrical Characteristics ($Ta = 25^{\circ}C$)

| Item | Symbol | Min | Тур | Max | Unit | Test conditions |
|---|-----------------------------|------|-------|-------|------|--|
| Drain to source breakdown voltage | $V_{(BR)DSS}$ | -60 | _ | _ | V | $I_{D} = -10 \text{ mA}, V_{GS} = 0$ |
| Gate to source breakdown voltage | $V_{(BR)GSS}$ | ±20 | _ | _ | V | $I_{G} = \pm 200 \ \mu A, \ V_{DS} = 0$ |
| Gate to source leak current | I _{GSS} | _ | _ | ±10 | μΑ | $V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$ |
| Zero gate voltage drain current | I _{DSS} | _ | _ | -250 | μΑ | $V_{DS} = -50 \text{ V}, V_{GS} = 0$ |
| Gate to source cutoff voltage | $V_{\text{GS(off)}}$ | -1.0 | | -2.25 | V | $I_{D} = -1 \text{ mA}, V_{DS} = -10 \text{ V}$ |
| Static drain to source on state | R _{DS(on)} | _ | 0.033 | 0.043 | Ω | $I_D = -15 \text{ A}, V_{GS} = -10 \text{ V}^{*1}$ |
| resistance | | _ | 0.045 | 0.06 | Ω | $I_D = -15 \text{ A}, V_{GS} = -4 \text{ V}^{*1}$ |
| Forward transfer admittance | y _{fs} | 17 | 25 | _ | S | $I_D = -15 \text{ A}, V_{DS} = -10 \text{ V}^{*1}$ |
| Input capacitance | Ciss | _ | 3300 | _ | pF | $V_{DS} = -10 \text{ V}, V_{GS} = 0,$ |
| Output capacitance | Coss | _ | 1500 | _ | pF | f = 1 MHz |
| Reverse transfer capacitance | Crss | _ | 480 | _ | pF | _ |
| Turn-on delay time | $\mathbf{t}_{\text{d(on)}}$ | _ | 30 | _ | ns | $I_{D} = -15 \text{ A}, V_{GS} = -10 \text{ V},$ |
| Rise time | t, | _ | 170 | _ | ns | $R_L = 2 \Omega$ |
| Turn-off delay time | $t_{\text{d(off)}}$ | _ | 500 | _ | ns | _ |
| Fall time | t _f | _ | 390 | _ | ns | _ |
| Body to drain diode forward voltage | V_{DF} | _ | -1.5 | _ | V | $I_{F} = -30 \text{ A}, V_{GS} = 0$ |
| Body to drain diode reverse recovery time | t _{rr} | _ | 200 | | ns | $I_F = -30 \text{ A}, V_{GS} = 0,$ $di_F/dt = 50 \text{ A}/\mu\text{s}$ |
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Note 1. Pulse test

See characteristic curves of 2SJ280



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