TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSII^{.5})

2SK1119

DC-DC Converter and Motor Drive Applications

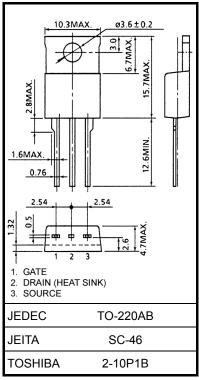
Unit: mm

 $\begin{array}{ll} \bullet & \text{Low drain-source ON resistance} & : \text{RDS (ON)} = 3.0 \ \Omega \ (\text{typ.}) \\ \bullet & \text{High forward transfer admittance} & : |Y_{fs}| = 2.0 \ S \ (\text{typ.}) \\ \bullet & \text{Low leakage current} & : I_{DSS} = 300 \ \mu\text{A (max)} \ (V_{DS} = 800 \ V) \\ \end{array}$

• Enhancement mode $V_{th} = 1.5 \sim 3.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA}$

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	1000	V	
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	1000	V	
Gate-source voltage		V_{GSS}	±20	V	
Drain current	DC (Note 1)	ΙD	4	Α	
	Pulse (Note 1)	I_{DP}	12	^	
Drain power dissipation	n (Tc = 25°C)	P_{D}	100	W	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 2.0 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.25	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

This transistor is an electrostatic-sensitive device.

Please handle with caution.



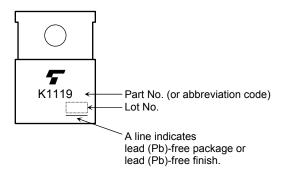
Electrical Characteristics (Ta = 25°C)

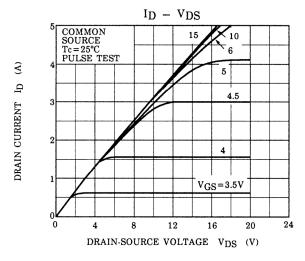
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±20 V, V _{DS} = 0 V	_	_	±100	nA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 800 V, V _{GS} = 0 V	_	_	300	μΑ
Drain-source br voltage	eakdown	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	1000	_	_	V
Gate threshold v	oltage/	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.5	V
Drain-source O	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 2 A	_	3.0	3.8	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 20 V, I _D = 2 A	1.0	2.0	_	S
Input capacitano	e	C _{iss}		_	700	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	55	_	pF
Output capacitance		Coss		_	100	_	
Switching time	Rise time	t _r	$V_{GS} \stackrel{10V}{_{0V}} \stackrel{I_{D}=2A}{_{R_{L}}} \stackrel{V_{OUT}}{\underset{=200\Omega}{}}$	_	18	_	
	Turn-on time	t _{on}		_	30	_	
	Fall time	t _f		_	12	_	ns
	Turn-off time	t _{off}	$V_{DD} = 400V$ Duty $\leq 1\%$, $t_w = 10 \mu s$	_	70	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	60		nC
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 6 \text{ A}$	_	35	_	
Gate-drain ("miller") charge		Q _{gd}]		25	_	

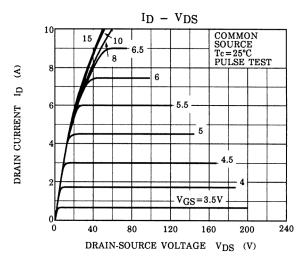
Source-Drain Ratings and Characteristics (Ta = 25°C)

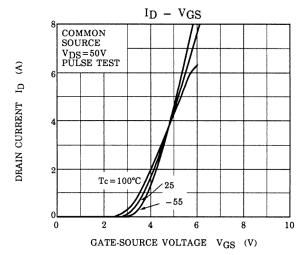
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	4	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	-	12	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 4 A, V _{GS} = 0 V	ı	1	-1.9	V

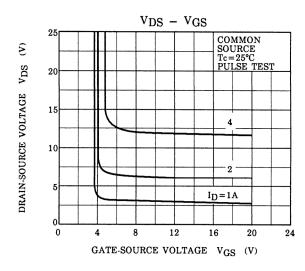
Marking

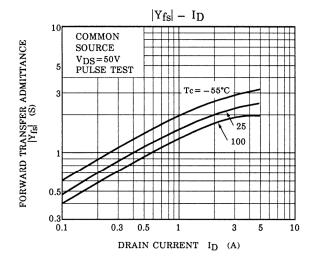


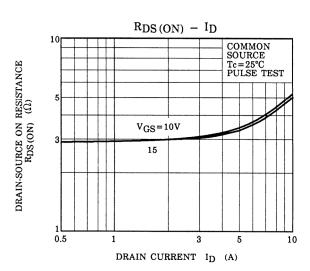




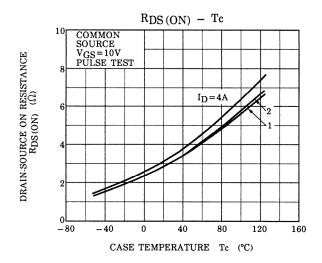


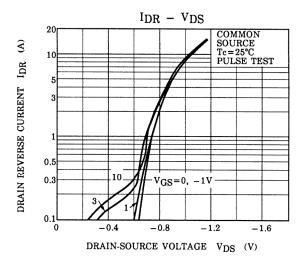


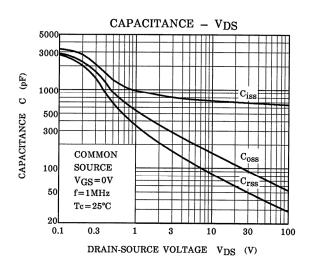


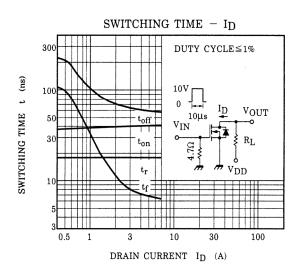


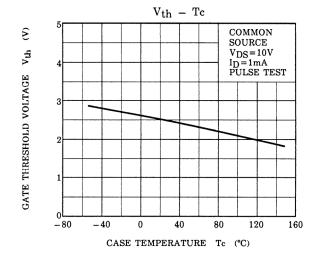
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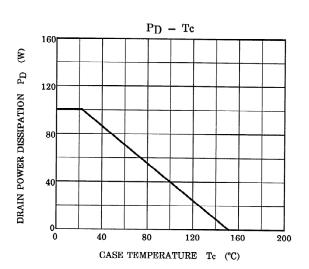


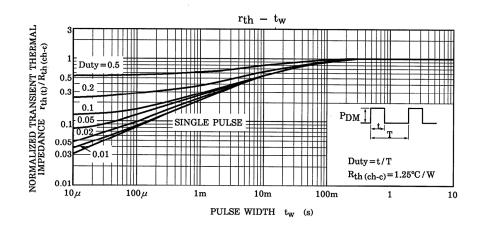


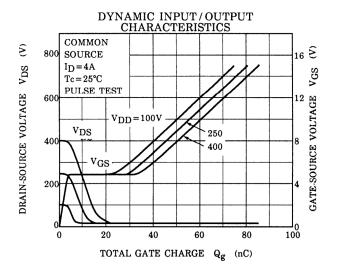


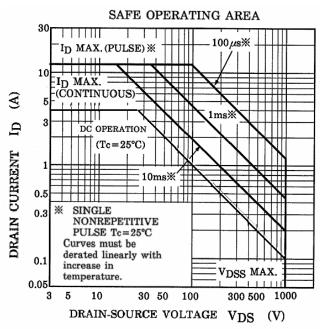












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