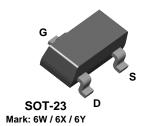


**J174** J175 **J176** J177

## MMBFJ175 MMBFJ176 MMBFJ177





NOTE: Source & Drain are interchangeable

### **P-Channel Switch**

This device is designed for low level analog switching sample and hold circuits and chopper stabilized amplifiers. Sourced from Process 88.

### **Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	- 30	V
V <sub>GS</sub>	Gate-Source Voltage	30	V
I <sub>GF</sub>	Forward Gate Current	50	mA
T <sub>J</sub> ,T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### **Thermal Characteristics** TA = 25°C unless otherwise noted

Symbol	Characteristic	М	Units	
		J174 -177	*MMBFJ175-177	
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	350 2.8	225 1.8	mW mW/∘C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	556	°C/W

<sup>\*</sup>Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

### P-Channel Switch

(continued)

E	lectr	ical	Cł	nara	acter	'isti	ics

TA = 25°C unless otherwise noted

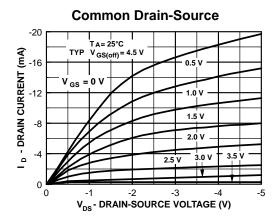
Symbol	Parameter	Test Conditions	Min	Max	Units	
	A OTEDIOTION					
OFF CHAI	RACTERISTICS				•	
$B_{(BR)GSS}$	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$		30		V
I <sub>GSS</sub>	Gate Reverse Current	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0			1.0	nA
V <sub>GS(off)</sub>	Gate-Source Cutoff Voltage	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 10 nA	174	5.0	10	V
(- ,			175	3.0	6.0	V
			176	1.0	4.0	V
			177	0.8	2.5	V

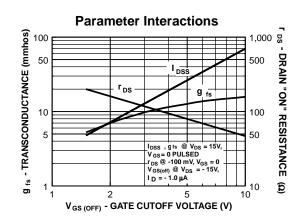
### **ON CHARACTERISTICS**

I <sub>DSS</sub>	Zero-Gate Voltage Drain Current*	V <sub>DS</sub> = - 15 V, I <sub>GS</sub> = 0	174 175 176	- 20 - 7.0 - 2.0 - 1.5	- 100 - 60 - 25 - 20	mA mA mA
r <sub>DS(on)</sub>	Drain-Source On Resistance	$V_{DS} \le 0.1 \text{ V}, V_{GS} = 0$	177 174 175 176 177	- 1.3	85 125 250 300	Ω Ω Ω

<sup>\*</sup>Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

## **Typical Characteristics**

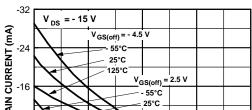




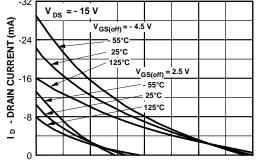
### P-Channel Switch

(continued)

### Typical Characteristics (continued)

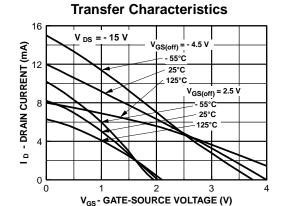


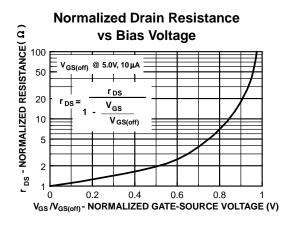
**Transfer Characteristics** 

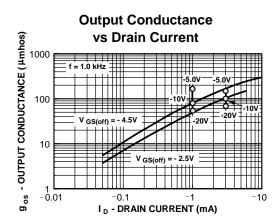


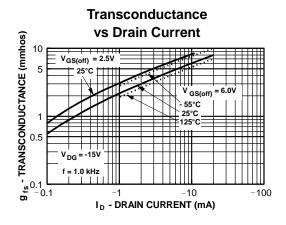
V<sub>GS</sub>- GATE-SOURCE VOLTAGE (V)

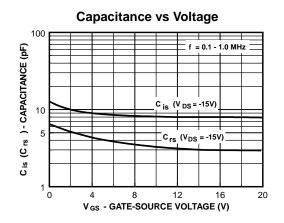
0







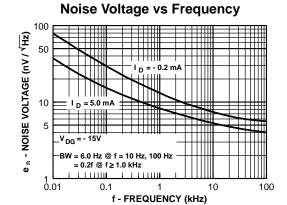


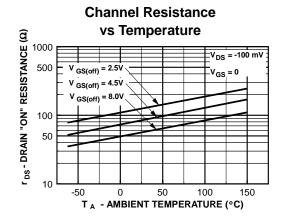


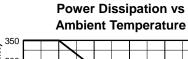
### **P-Channel Switch**

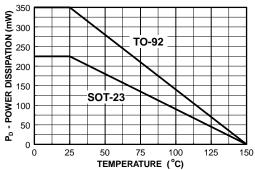
(continued)

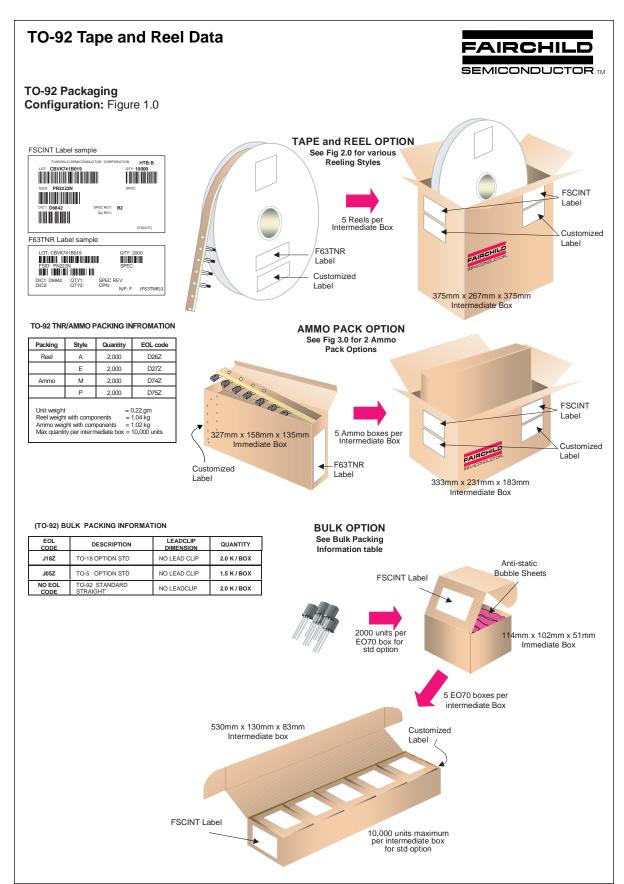
### Typical Characteristics (continued)







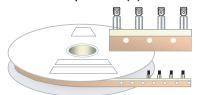




### TO-92 Tape and Reel Data, continued

### **TO-92 Reeling Style** Configuration: Figure 2.0

### Machine Option "A" (H)

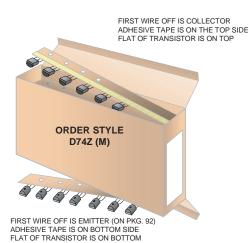


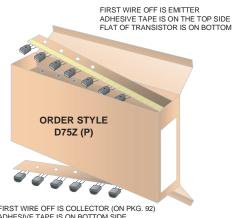
Style "A", D26Z, D70Z (s/h)

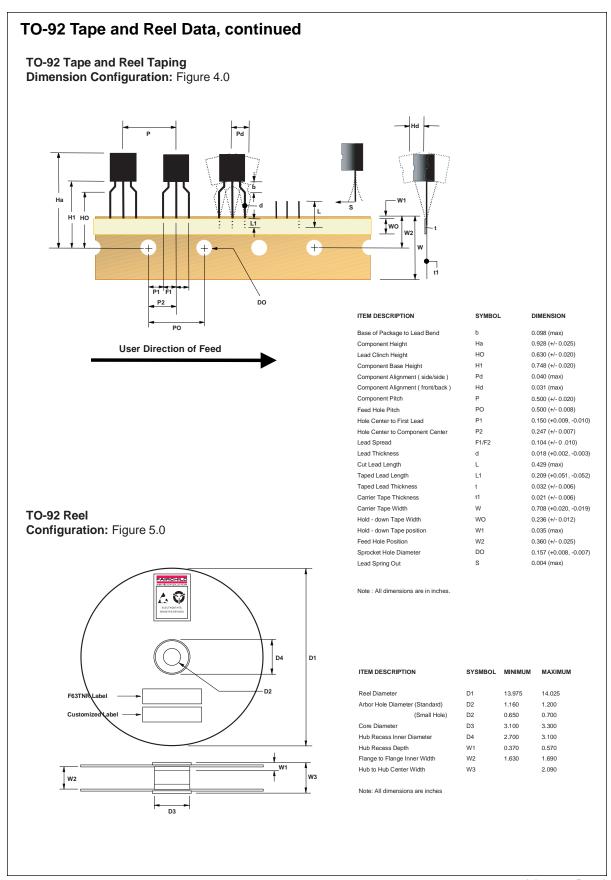
# Machine Option "E" (J)

Style "E", D27Z, D71Z (s/h)

### **TO-92 Radial Ammo Packaging** Configuration: Figure 3.0



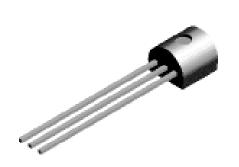


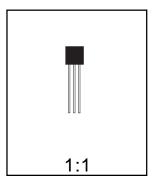


### **TO-92 Package Dimensions**



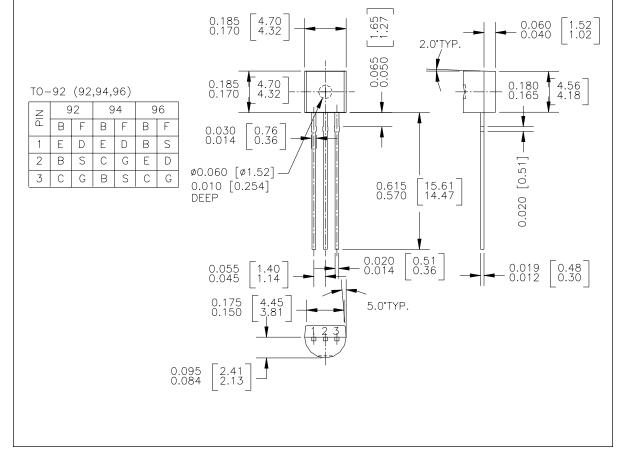
## TO-92 (FS PKG Code 92, 94, 96)

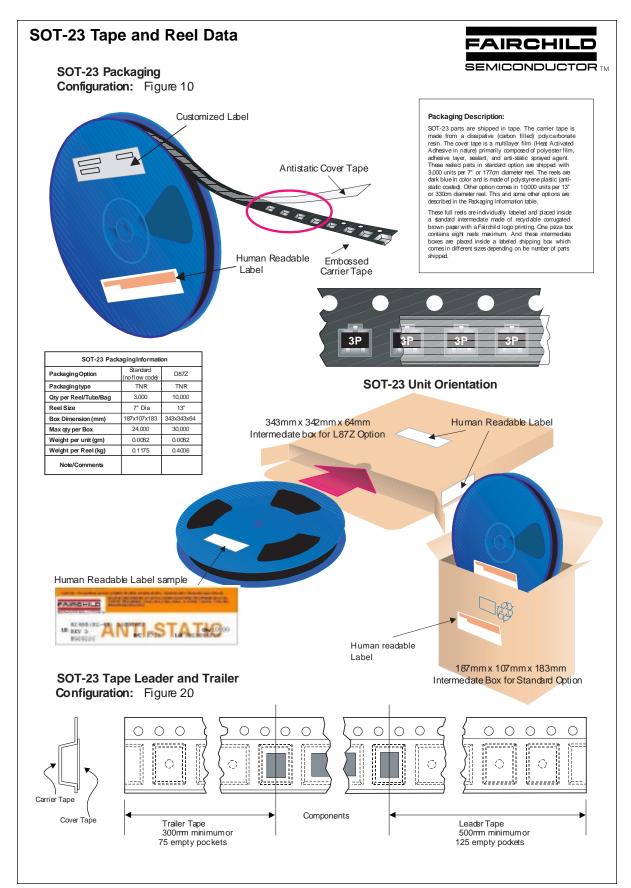




Scale 1:1 on letter size paper
Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.1977

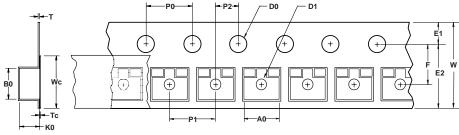




### SOT-23 Tape and Reel Data, continued

### **SOT-23 Embossed Carrier Tape**

Configuration: Figure 3.0



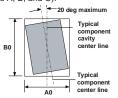
User Direction of Feed

					Di	mension	s are in n	nillimete	r					
Pkg type	Α0	В0	w	D0	D1	E1	E2	F	P1	P0	K0	Т	Wc	Тс
<b>SOT-23</b> (8mm)	3.15 +/-0.10	2.77 +/-0.10	8.0 +/-0.3	1.55 +/-0.05	1.125 +/-0.125	1.75 +/-0.10	6.25 min	3.50 +/-0.05	4.0 +/-0.1	4.0 +/-0.1	1.30 +/-0.10	0.228 +/-0.013	5.2 +/-0.3	0.06 +/-0.02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



Sketch A (Side or Front Sectional View)
Component Rotation

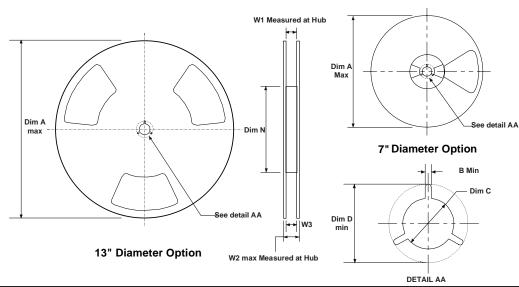


Sketch B (Top View)
Component Rotation



Sketch C (Top View)
Component lateral movement

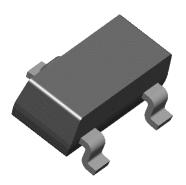
### SOT-23 Reel Configuration: Figure 4.0

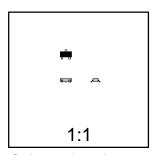


	Dimensions are in inches and millimeters								
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
8mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9
8mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9



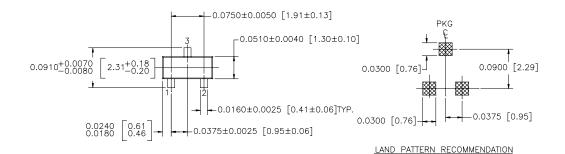
## SOT-23 (FS PKG Code 49)

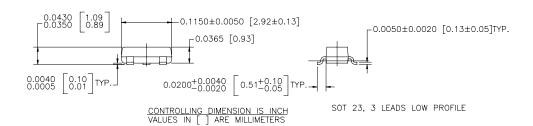




Scale 1:1 on letter size paper Dimensions shown below are in:

inches [millimeters]
Part Weight per unit (gram): 0.0082





NOTE: UNLESS OTHERWISE SPECIFIED

- 1. STANDARD LEAD FINISH 150 MICROINCHES / 3.81 MICROMETERS MINIMUM TIN / LEAD (SOLDER) ON ALLOY 42
- 2. REFERENCE JEDEC REGISTRATION TO-236, VARIATION AB, ISSUE G, DATED JUL 1993

### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

 $ACEx^{TM}$ FASTr™ PowerTrench® SyncFET™ Bottomless™ QFET™ TinyLogic™ GlobalOptoisolator™ QSTM UHC™ CoolFET™ GTO™ **VCX**<sup>TM</sup>  $CROSSVOLT^{TM}$ QT Optoelectronics™ HiSeC™

DOME™ ISOPLANAR™ Quiet Series™

### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

### PRODUCT STATUS DEFINITIONS

### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.